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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Yasuhiro ICHIHARA et al.

Application No.: 09/714,225

Group Art Unit: 2882

Confirmation No.: 4184

Filed: November 17, 2000

Examiner: Wang, G.

For: LASER DIODE MODULE AND ASSEMBLING METHOD THEREFOR

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action mailed March 25, 2002, applicants elect Claims 1-3, 6-7, and 11-13 of Group I, without traverse.

If any further fees are required in connection with the filing of this Response, please charge our Deposit Account No. 19-3935.

Respectfully submitted,

STAAS & HALSEY LLP

Date: April 19, 2002

By: [Signature]
Gene M. Garner II
Registration No. 34,172

700 11th Street, N.W., Ste. 500
Washington, D.C. 20001
(202) 434-1500

TECHNOLOGY CENTER 2800

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Jesse